

Power System Engineering, Inc.

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# IEEE 1547 – Synchronization

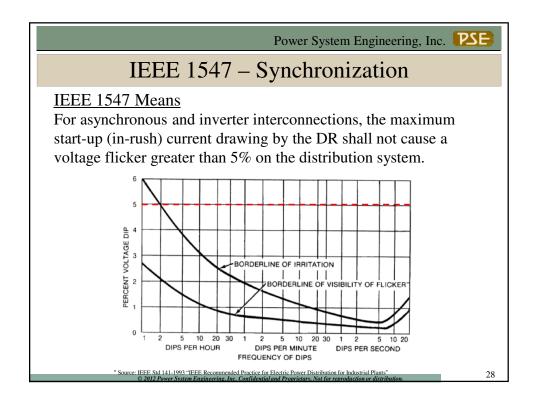
#### IEEE 1547 Means

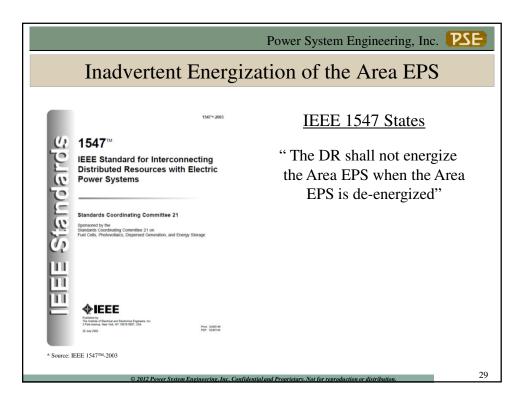
For synchronous and inverter-based interconnection systems that produce a fundamental voltage before paralleling, the following synchronization limits shall apply:

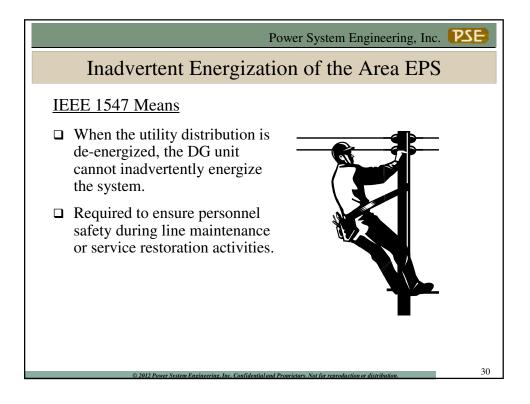
Table 5—Synchronization parameter limits for synchronous interconnection to an EPS, or an energized local EPS to an energized Area EPS

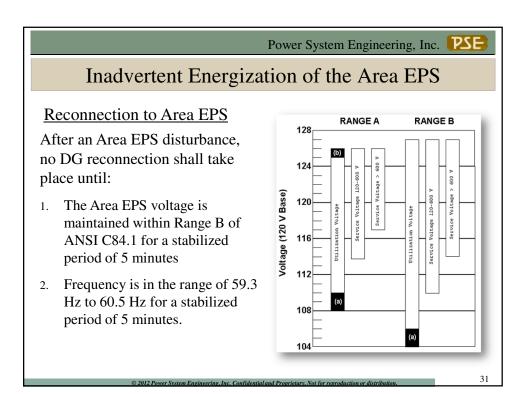
Aggregate rating of DR units (kVA)	Frequency difference (Δf, Hz)	Voltage difference (ΔV, %)	Phase angle difference (ΔΦ, °)
0 - 500	0.3	10	20
> 500 - 1 500	0.2	5	15
> 1 500 - 10 000	0.1	3	10

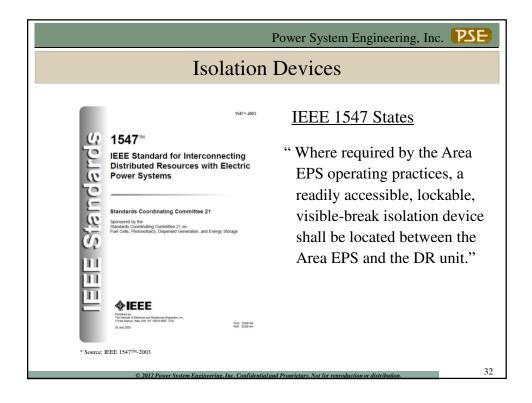
\* Source: IEEE 1547TM-2003

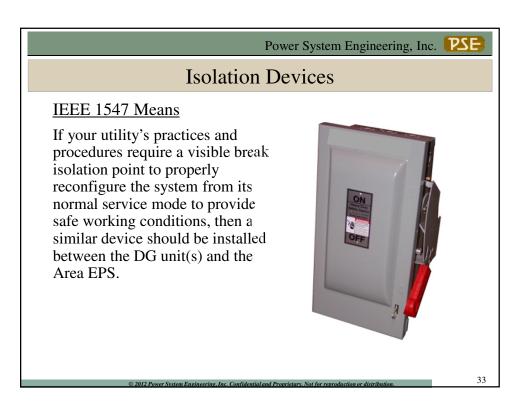


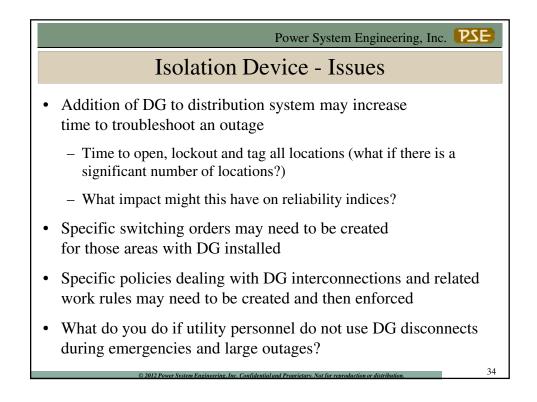


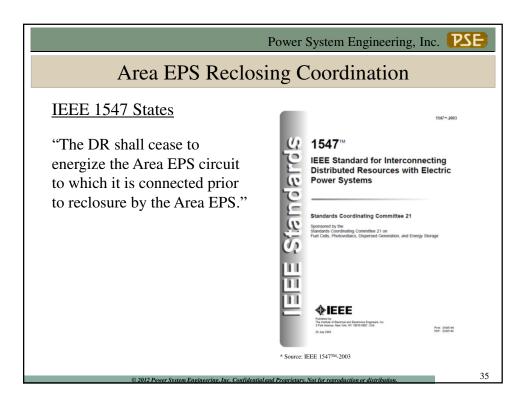


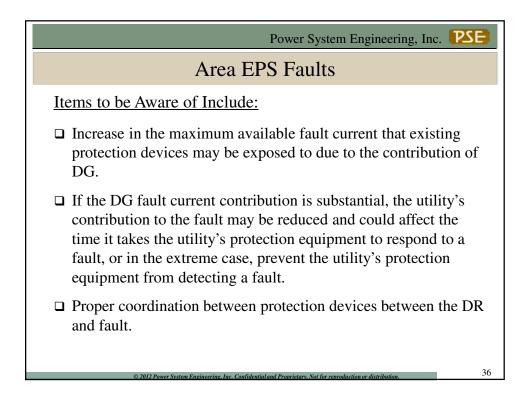


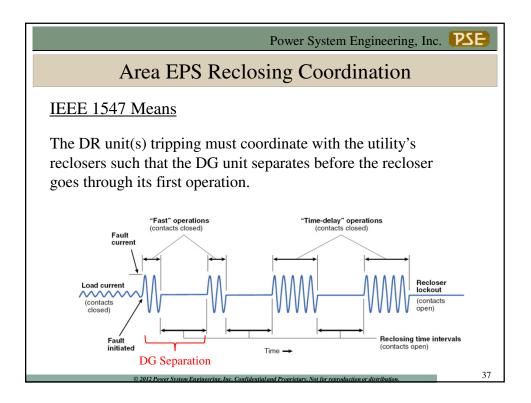


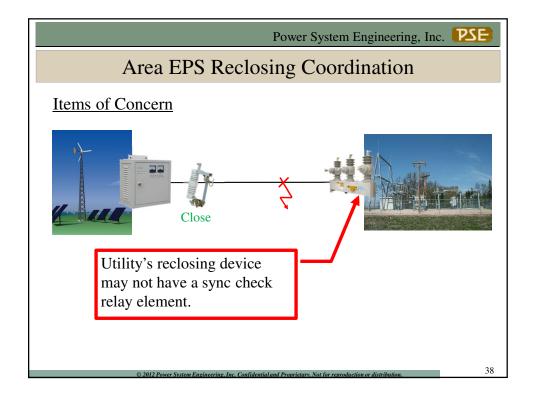












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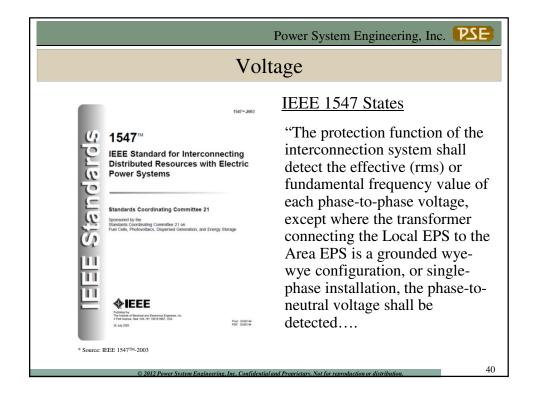
# Area EPS Reclosing Coordination

### Items of Concern

If a fault is temporary in nature and the DG unit does not cease to energize during a reclosing event on the distribution system, the fault arc will most likely not have a change to extinguish.



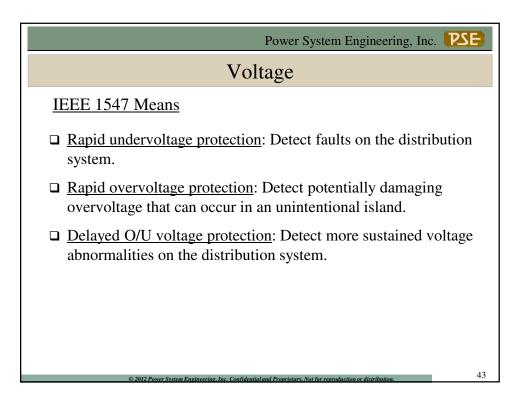
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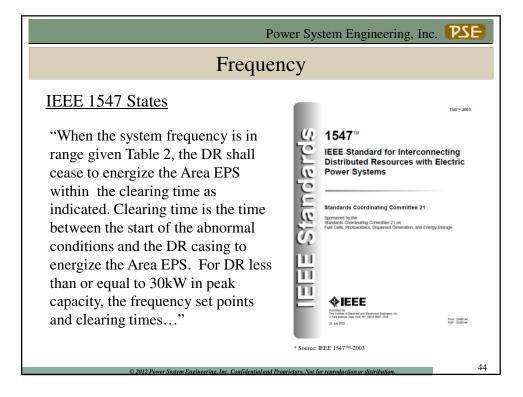


Power System Engineering, Inc.	
Voltage	
IEEE 1547 States	
"when any voltage is in a range given in Table 1, the DR shall cease to energize the Area EPS within the clearing time as indicated. Clearing time is the time between the start of the abnormal condition and the DR ceasing to energize the Area EPS. For DR less than or equal to 30kW in peak capacity, the voltage set points and clearing times shall be either fixed or field adjustable. For DR greater than 30kW, the voltage set points shall be field adjustable."	

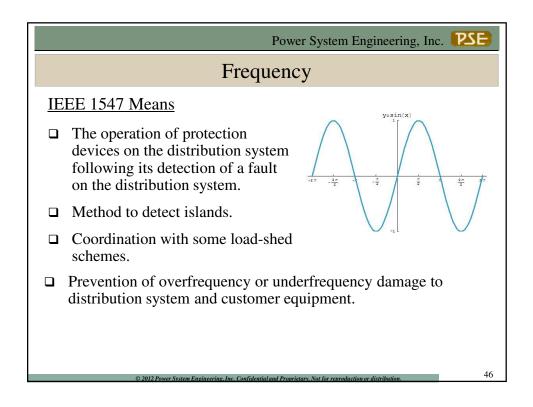
Power System Engineering, Inc. Voltage Table 1—Interconnection system response to abnormal voltages Voltage range Clearing time(s)<sup>b</sup> (% of base voltage<sup>a</sup>)  $V \le 50$ 0.16 2.00  $50 \leq \mathrm{V}\!\!< 88$  $110 < \rm V < 120$ 1.00  $V \ge 120$ 0.16 <sup>a</sup>Base voltages are the nominal system voltages stated in ANSI C84.1-1995, Table 1.  $^{b}$ DR  $\leq$  30 kW, maximum clearing times; DR > 30kW, default clearing times. \* Source: IEEE 1547<sup>TM</sup>-2003 42 er System Engineering, Inc. Confidential and Proprietary. Not for

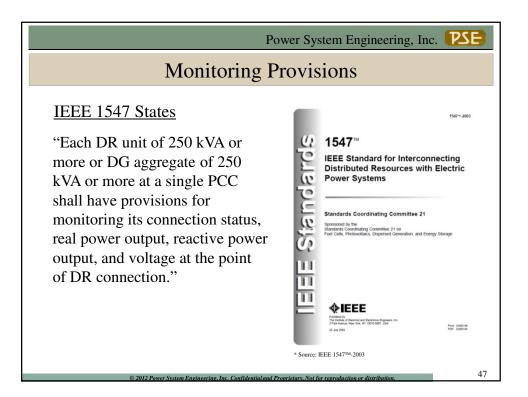
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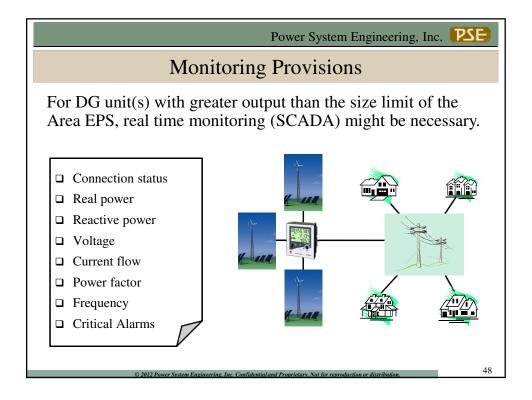


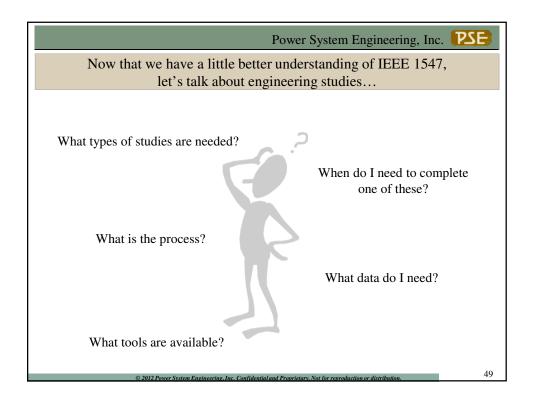


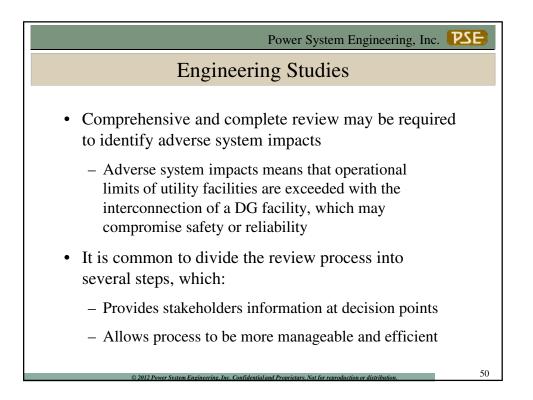
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		Frequency	,	
<u>IEEE 15</u>	547 States			
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0	S operations Table 2—Interco	" nnection system response to Frequency range (Hz)	abnormal frequencies Clearing time(s) <sup>a</sup>	ated with
0	S operations Table 2—Interco DR size	Prequency range (Hz) > 60.5	abnormal frequencies Clearing time(s) <sup>a</sup> 0.16	ated with
0	S operations Table 2—Interco DR size	Prequency range (Hz)	Clearing time(s) <sup>a</sup> 0.16 0.16	ated with

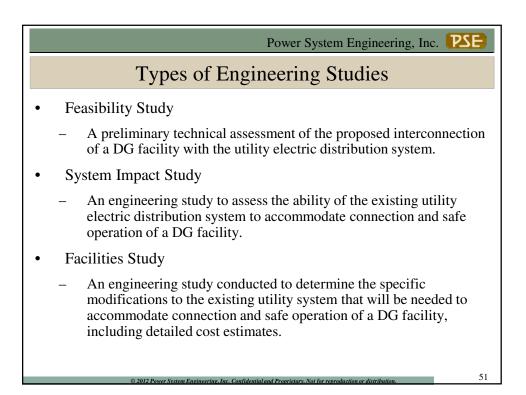


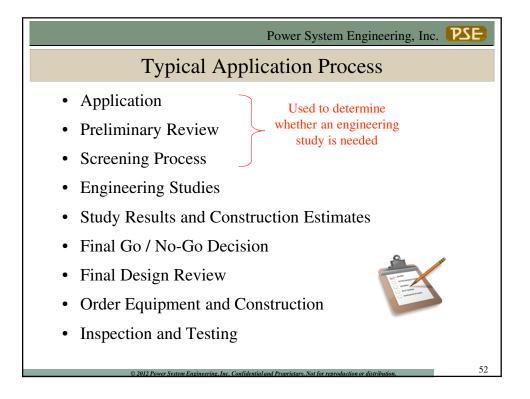


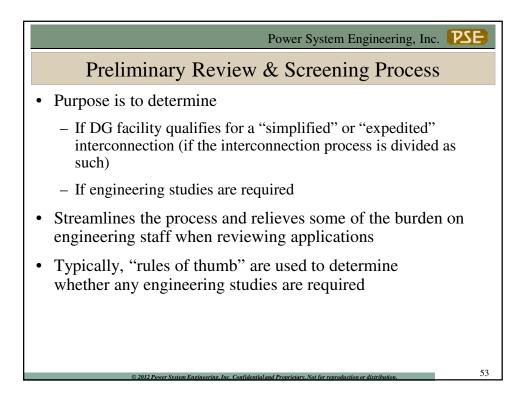


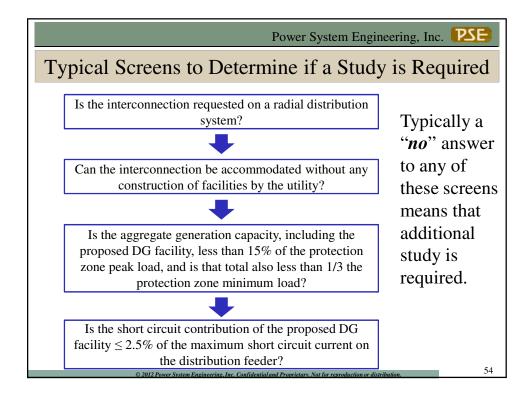


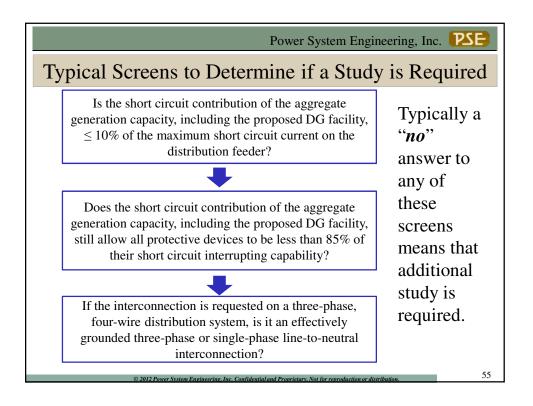


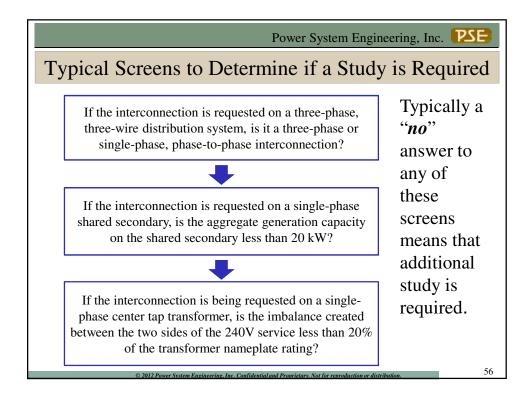


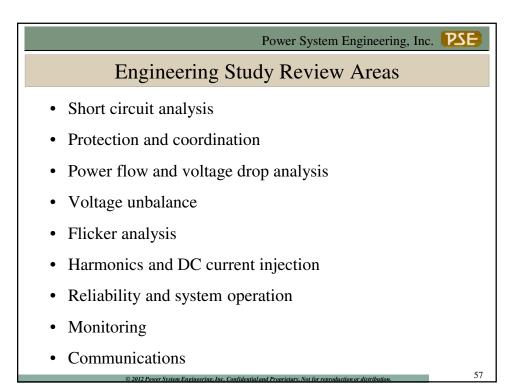


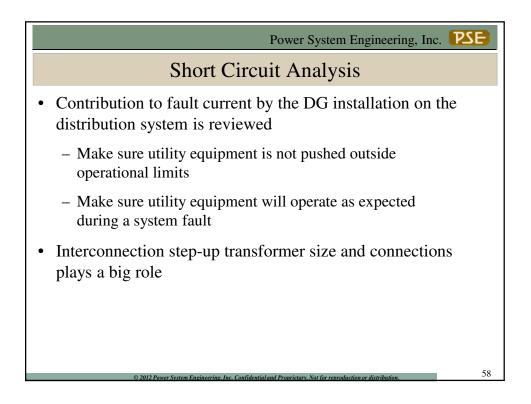


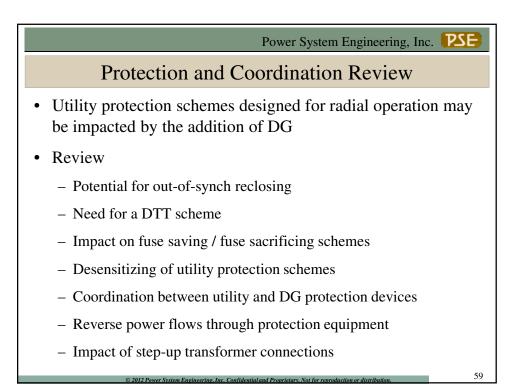


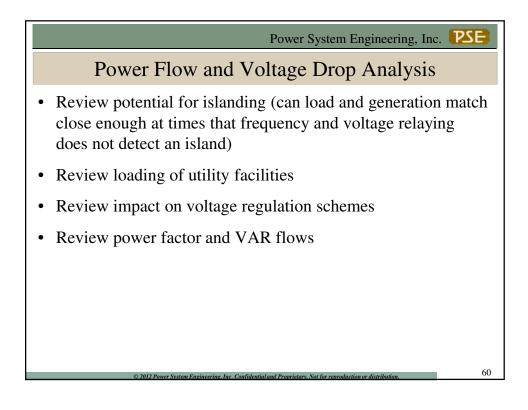


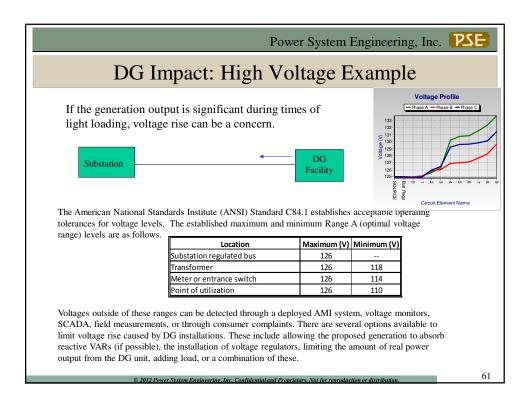












Power System Engineering, Inc.	PSE			
Flicker Analysis				
• Voltage swings during DG start-up and when DG goes offline				
<ul> <li>Large wind turbines can be of particular concern</li> </ul>				
<ul> <li>Synchronous generators can be operated to limit voltage swings if they trip offline</li> </ul>				
• Flicker associated with variable wind speeds and cloud cover				
• Perception of "flicker" is based on magnitude of the voltage swing and the frequency of events				
<ul> <li>Maximum voltage swing allowed should be defined in technical requirements (typically in the range of 3–6%)</li> </ul>				
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